

specification.

"Alarm means" in Claim 37, "interference stop instruction means" in Claim 38 and "means for stopping the interference" in Claim 38 all correspond to "CPU 5." The functions as "alarm means," as "interference stop instruction means" and as "means for stopping the interference" are described at page 15, lines 24-26, at page 26, lines 12-19 and at page 26, lines 20-23, respectively, in the specification.

Claims 21-40 are rejected under the judicially doctrine of double patenting over claims 1-20 of copending Application No. 08/555,400. With regard to this matter, a terminal disclaimer is filed separately, and the same is believed to overcome the raised rejection on double patenting.

As mentioned above, it is respectfully submitted that the above-identified application is now in condition for allowance, and the early allowance of the above-identified application is respectfully requested.

Respectfully submitted,

  
Takeya OKAMOTO, applicant